

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1
2 Priority Application Serial No. 09/146,842
3 Priority Filing Date September 3, 1998
4 Inventor Zhiping Yin et al.
5 Assignee Micron Technology, Inc.
6 Priority Group Art Unit 2824
7 Priority Examiner B. Smith
8 Attorney's Docket No. MI22-1427
9 Title: Circuitry and Gate Stacks (As Amended)

PRELIMINARY AMENDMENT

10 To: Assistant Commissioner for Patents
11 Washington, D.C. 20231
12
13 From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
14 Wells, St. John, Roberts, Gregory & Matkin P.S.
15 601 W. First Avenue, Suite 1300
16 Spokane, WA 99201-3828

AMENDMENTS

In the Specification

17 Replace the title with --Circuitry and Gate Stacks--.

18 At p. 1, before the "Technical Field" section, insert the following:

--RELATED PATENT DATA

19 This patent resulted from a divisional application of U.S. Patent
20 Application Serial No. 09/146,842, filed September 3, 1998,--
now Patent No. 6,281,100 ✓